

D
no resistor connected between said transistors and ground, to discharge static electricity through said transistors to ground and avoid reduction in gain.

6. (Twice Amended) An ESC (Electro-Static-Discharge) protection circuit comprising:

D
a pad, a main chip, and a connector, said connector directly shorting said pad and main chip; and

D
a plurality of transistors, each connected between the pad and the main chip, having resistors connected to an input terminal only, to discharge static electricity through said transistors to ground and avoid reduction in gain.
